

What is claimed is:

1. A memory access control apparatus comprising:
 - a memory access control unit for storing image data in a memory by a two-dimensional array according to values of a bank, a row, and a column inside the memory where the image data is to be stored calculated on the basis of coordinate values of the image data constituting one image frame and predetermined data.
- 10 2. The apparatus of claim 1 further comprising a storing unit for storing the predetermined data.
- 15 3. The apparatus of claim 1, wherein the predetermined data is a word per bank, a row per unit line, an offset, and a base row value.
4. The apparatus of claim 3, wherein the word per bank is the number of words stored in each line of the N^{th} bank of the N^{th} row inside the memory.
- 20 5. The apparatus of claim 3, wherein the row per unit line is the number of rows inside the memory where one image line inside one image frame is stored.
- 25 6. The apparatus of claim 5, wherein the unit line is the number of lines stored in the N^{th} bank of the N^{th} row inside the memory.

7. The apparatus of claim 3, wherein the offset is obtained by multiplying a vertical line/a unit line to a row per unit line.

8. The apparatus of claim 7, wherein the vertical line is the number
5 of lines inside the memory where one image frame is stored.

9. The apparatus of claim 7, wherein the unit line is the number of
lines stored in the N^{th} bank of the N^{th} row inside the memory.

10. The apparatus of claim 3, wherein the base row value is a start
row address of one image frame.

11. The apparatus of claim 1, wherein the memory access control unit
stores image data of a horizontal direction inside said one image frame in the N^{th}
15 row inside the memory in a horizontal direction, and stores image data of a vertical
direction inside said one image frame in the N^{th} row inside the memory in a vertical
direction.

12. The apparatus of claim 11, wherein the memory access control
20 unit stores image data of 256 words in the N^{th} bank of the N^{th} row inside the
memory.

13. The apparatus of claim 12, wherein the memory access control
unit stores the image data in the N^{th} bank of the N^{th} row inside the memory so that
25 a word per bank can be 32 and a unit line can be 8.